L Number	Hits	Search Text	DB	Time stamp
-	2	(348/294.ccls.) and (ramp NEAR voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 09:28
-	20	(348/294.ccls.) and (accelerat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 12:27
-	14	chan NEAR ki NEAR kim	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 09:35
-	195	(CMOS near image$2) and (accelerat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 12:26
-	2	(CMOS near image$2) and (accelerat$4) AND (bit NEAR line)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 12:26
-	19	(348/302.ccls.) and (accelerat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 12:41
-	12	accelerat$4 near bit NEAR lin$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 12:35
-	25	(348/302.ccls.) and (bit NEAR lin$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 15:53
-	4	(348/302.ccls.) and (bit NEAR lin$4) AND (PMOS) AND (NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 12:05
-	12	(348/$6.ccls.) and (accel$6 NEAR circuit$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/31 17:02
-	61	(PMOS) and (accel$6 NEAR circuit$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 09:45
-	2723	341/155.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 09:45
-	34	341/155.ccls. AND (accelerat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 09:48
-	3	341/155.ccls. AND (accelerat$4) AND (PMOS OR NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 09:52

-	0	341/172.ccls. AND (accelerat$4) AND (PMOS OR NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 09:53
-	3	341/172.ccls. AND (accelerat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 09:53
-	272	348/302.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 12:21
-	461	348/308.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/02 09:45
-	1	348/308.ccls. AND (accel$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 12:07
-	37	348/308.ccls. AND (bit NEAR lin$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 12:07
-	188	348/308.ccls. AND (speed$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 12:08
-	39	348/302.ccls. AND (PMOS OR NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 12:30
-	2	348/302.ccls. AND (bit NEAR line NEAR voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 12:30
-	2	348/308.ccls. AND (bit NEAR line NEAR voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 13:07
-	6	341/172.ccls. AND ((bit NEAR lin$3) same voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 13:09
-	2	348/308.ccls. AND (bit NEAR lin$3 NEAR voltage)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 13:07
-	3	348/308.ccls. AND (bit NEAR lin$3 NEAR volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 13:08
-	3	348/302.ccls. AND (bit NEAR lin$3 NEAR volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 13:09

-	6	341/172.ccls. AND ((bit NEAR lin$3) same volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 13:09
-	13	341/155.ccls. AND ((bit NEAR lin$3) same volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 15:02
-	59	341/172.ccls. AND (PMOS OR NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 14:09
-	98	341/155.ccls. AND (PMOS OR NMOS)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 14:10
-	14	326/95.ccls. AND ((bit NEAR lin$3) same volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 15:03
-	388	326/95.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 15:03
-	700	326/98.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 15:03
-	2597	365/203.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 15:03
-	1206	365/203.ccls. AND ((bit NEAR lin$3) same volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 15:06
-	173	365/203.ccls. AND (accelerat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 16:50
-	8	365/203.ccls. AND (accelerat$4 NEAR circuit$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 18:01
-	22	326/98.ccls. AND ((bit NEAR lin$3) same volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 16:51
-	5	365/203.ccls. AND (accelerat$4 NEAR volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 16:51
-	1	326/95.ccls. AND (accelerat$4 NEAR volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 16:56

-	4	326/98.ccls. AND (accelerat$4 NEAR volt$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 17:17
-	14	chan NEAR Ki NEAR KIM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 17:24
-	1	6423957.URPN.	USPAT	2004/04/01 17:20
-	2	("5877715" | "6115066").PN.	USPAT	2004/04/01 17:20
-	188	bit NEAR lin$3 NEAR variation	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 18:05
-	4	(bit NEAR lin$3 NEAR variation) AND accel$5	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 18:06
-	24	(bit NEAR lin$3 NEAR variation) AND accelerat$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/01 18:06
-	0	(5671181.pn.) and analog	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/02 09:45
-	0	(5671181.pn.) and A/D	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/02 09:45
-	1	(5671181.pn.) and digital	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/02 09:46
-	0	(5671181.pn.) and ramp	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/02 09:47
-	1	(5671181.pn.) and latch	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/02 09:59
-	1	(5671181.pn.) and "914"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/02 10:00